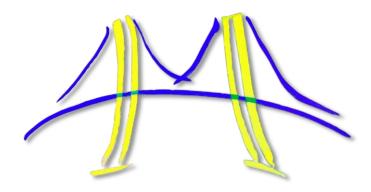
#### The ParLab Stack

Parallel Computing Laboratory

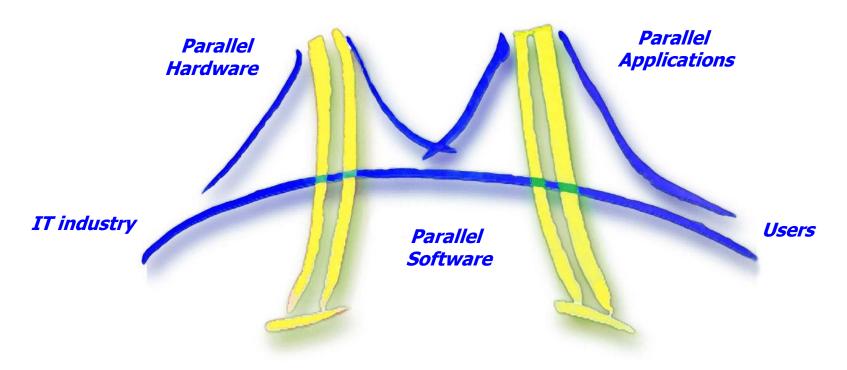
Sarah Bird

May 30, 2013



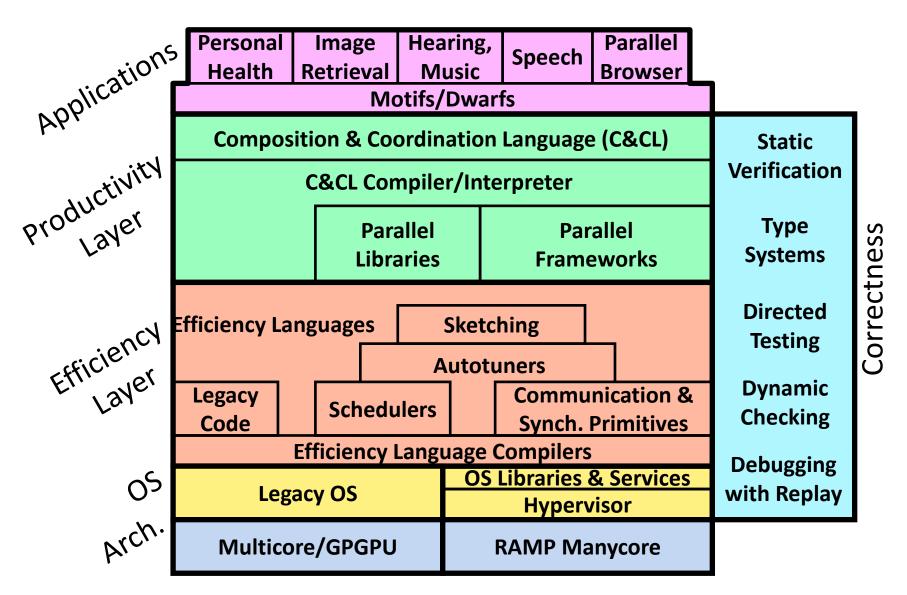


## Bridging the Gap



Easy to write correct programs that run efficiently on manycore

## Integrated Software Stack



#### Hardware

- RAMP Gold
  - Simulation
  - 64 cores
  - FPGA

- RISC-V
  - Implementations written in Chisel
  - Rocket
    - 6 stage in-order
  - Hwacha
    - 64 bit vector core
  - FPGA
    - 2 Rocket
  - 45 nm Chip
    - 1 Rocket, 1 Hwacha
    - 1 Ghz

- x86
- GPGPU
  - Cuda
  - OpenGL

**SPARC** 

**RISC-V** 

x86

PTX

**RAMP Gold** 

**FPGA** 

Chip

Multicore

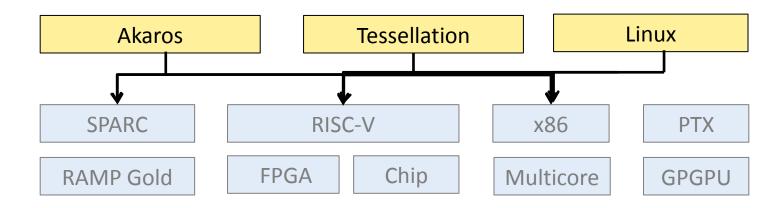
**GPGPU** 

## **Operating Systems**

- Akaros
  - Cloud OS
- Tessellation
  - Client OS
  - Space-Time Partitioning

Linux

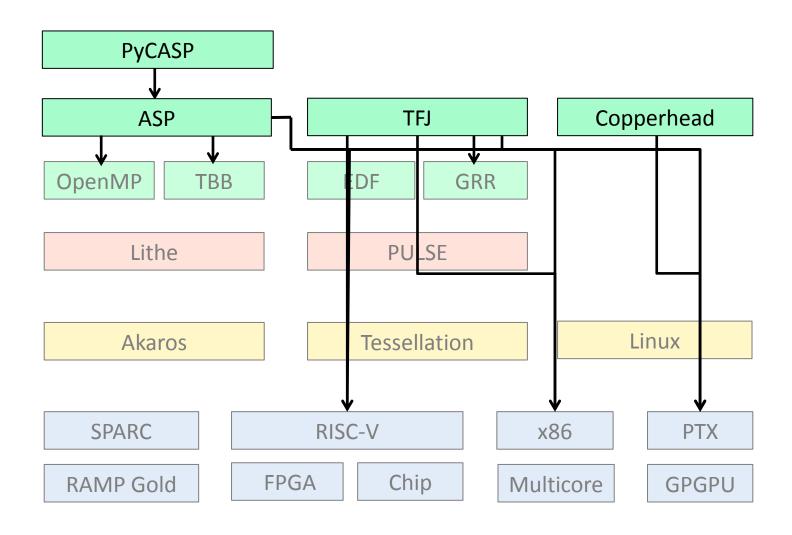
- Two-Level Scheduling
- QoS to Applications
- PACORA



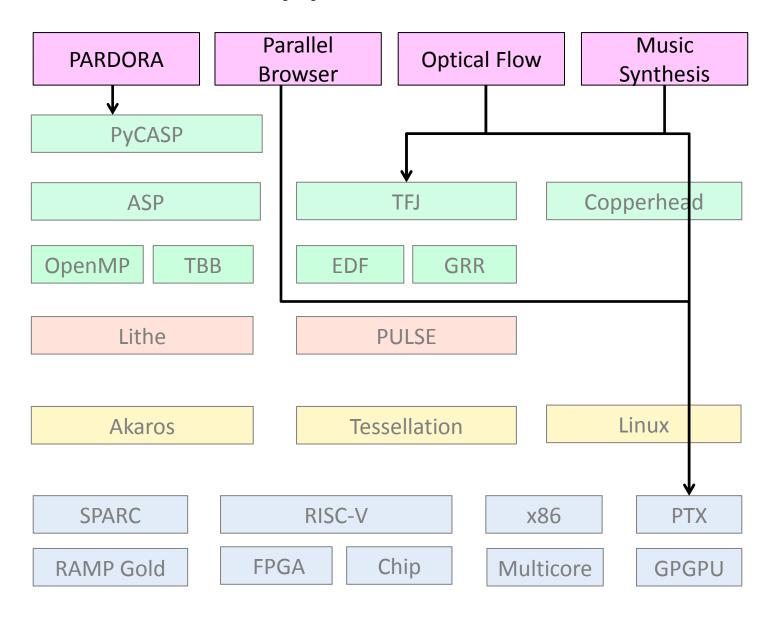
#### Schedulers

**PULSE** Lithe Framework to write schedulers **Compose Parallel Runtimes**  Earliest Deadline-First Thread Building Blocks Global Round Robin Open MP OpenMP **TBB** EDF GRR Lithe **PULSE** Linux **Akaros** Tessellation **SPARC** RISC-V x86 PTX **FPGA** Chip RAMP Gold Multicore **GPGPU** 

#### **SEJITS**



## **Applications**



## Why Create an Integrated Prototype?

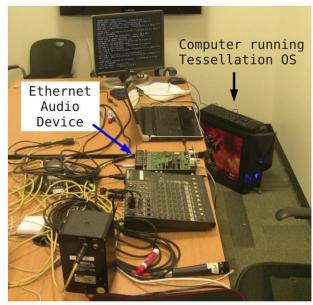
- Encourages Collaboration
- Prevents neglecting important pieces of the problem
- Uncover opportunities for invention by seeing which side of an interface is the best place to satisfy a requirement



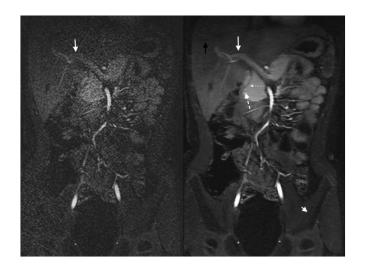
- Demonstrate the importance of design simplicity
- Enhance the education of the PhD students in areas beyond their own specialties
- Help with technology transfer by giving concrete examples of our ideas for our colleagues in industry

## Forces for Integration

- Design Compatibility
  - Shared Space and Discussions
  - Symbiotic Designs
  - Example: Music and Tessellation
- Customized Support
  - In-house experts helping adapt their design to your problem
  - Examples: Lithe and Tessellation
     CAA and Applications



Preparing the Music Demo on Tessellation OS for the Winter 2011 Par Lab Retreat.



- Motivating Applications
  - Exciting to show your research on run a compelling application
  - Examples: Patterns and MRI
     BFS and RISC-V

## Integrated Demos in ParLab History

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## Stack Redesign

What did rethinking the entire computing stack at once get us?

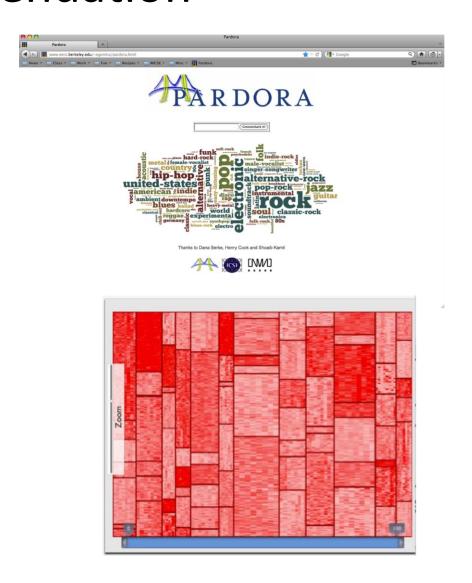
- Productivity programs can create applications that require efficiency
  - Scale
  - Performance
- Easily target many platforms and features
  - Example: Vector units on RISC-V Chip
- Efficient performance predictibility
  - Interactivity and Responsiveness
  - Realtime Performance

## **Integrated Demos**

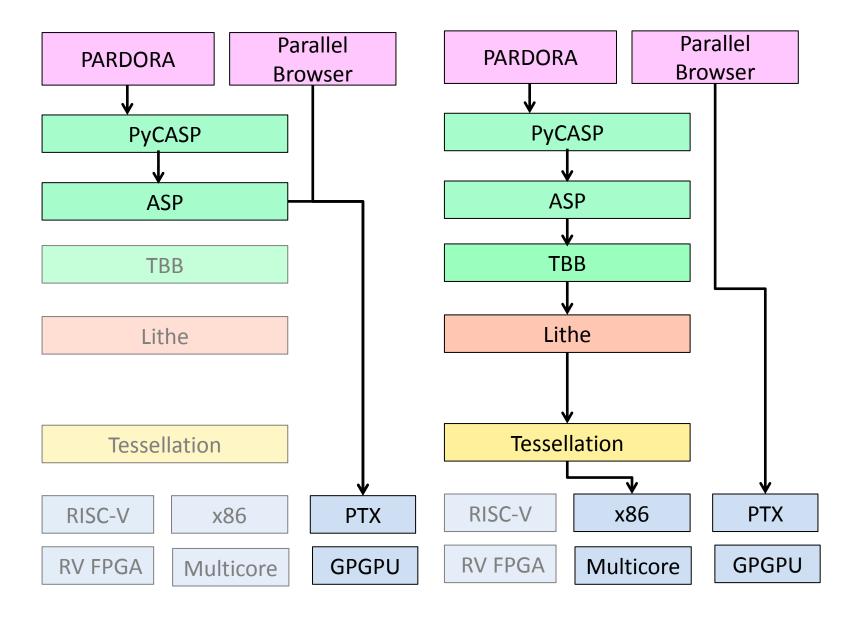
- Two Demos to show off the ParLab Stack
- Fun and compelling applications that require efficiency
- Easily target many platforms and features
- Interactivity and Realtime Performance
- Integration!

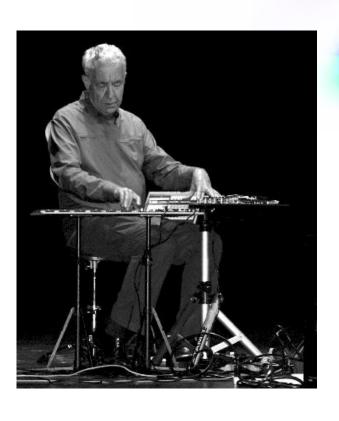
## Music Exploration and Recommendation

- Better Pandora
- Audio Content
   Analysis Framework
- Parallel Browser Big Data Visualization
- Demonstrates Scale and Responsiveness

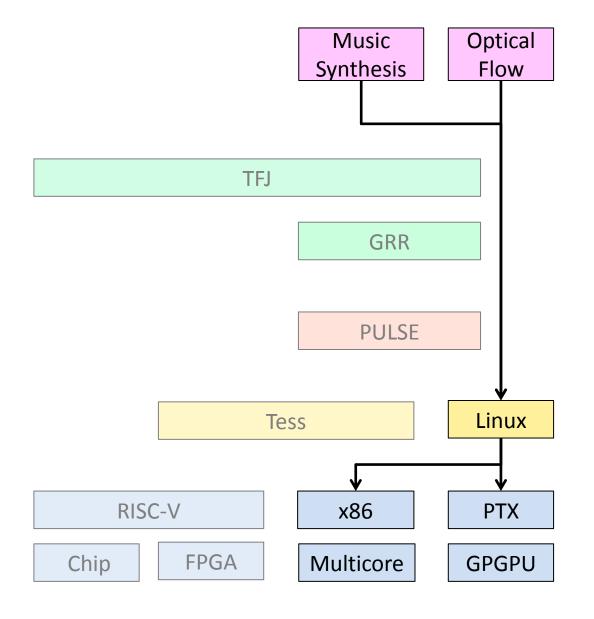


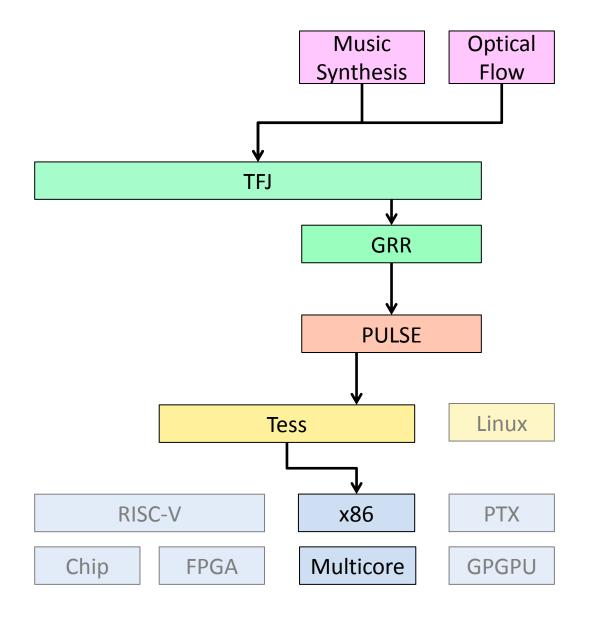
#### Music Recommendation and Exploration

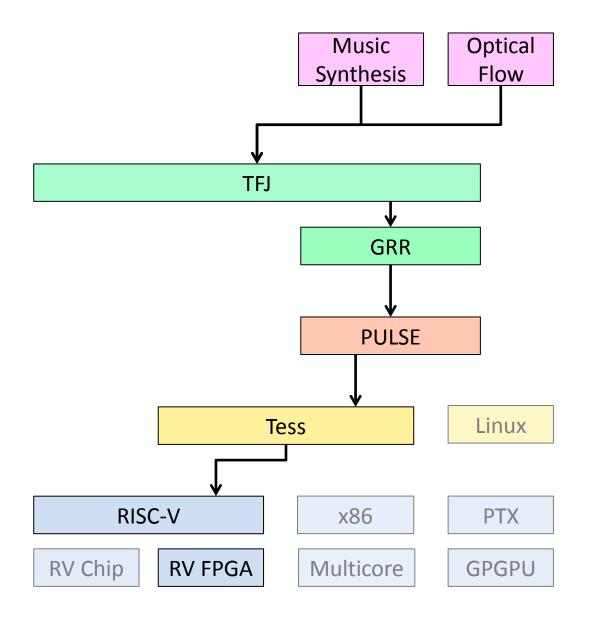


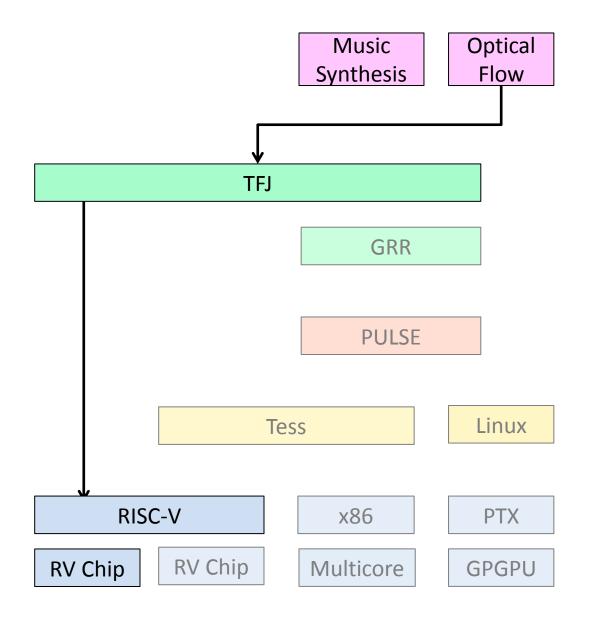


- Musical Instrument using a camera
- Music Synthesis
- Vision Applications
- DemonstrateRealtime







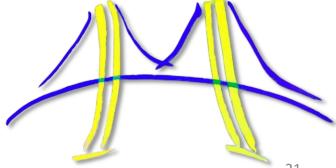


# Music Recommendation & Exploration Demo

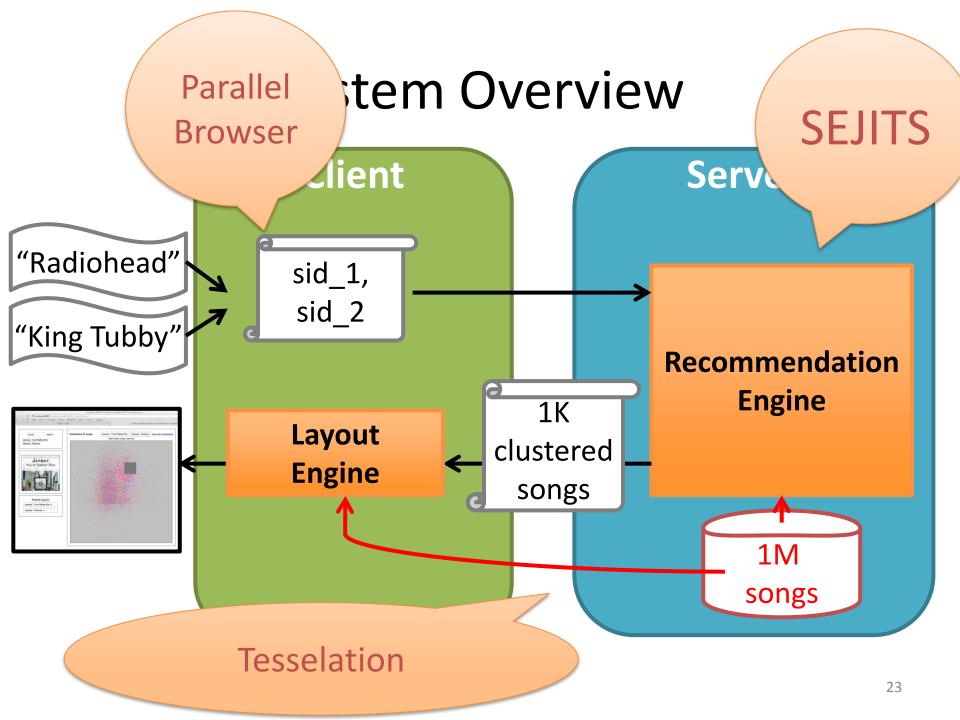
Leo Meyerovich, Katya Gonina, Gage Eads, Eric Roman, Eric Battenberg, Henry Cook, Gerald Friedland

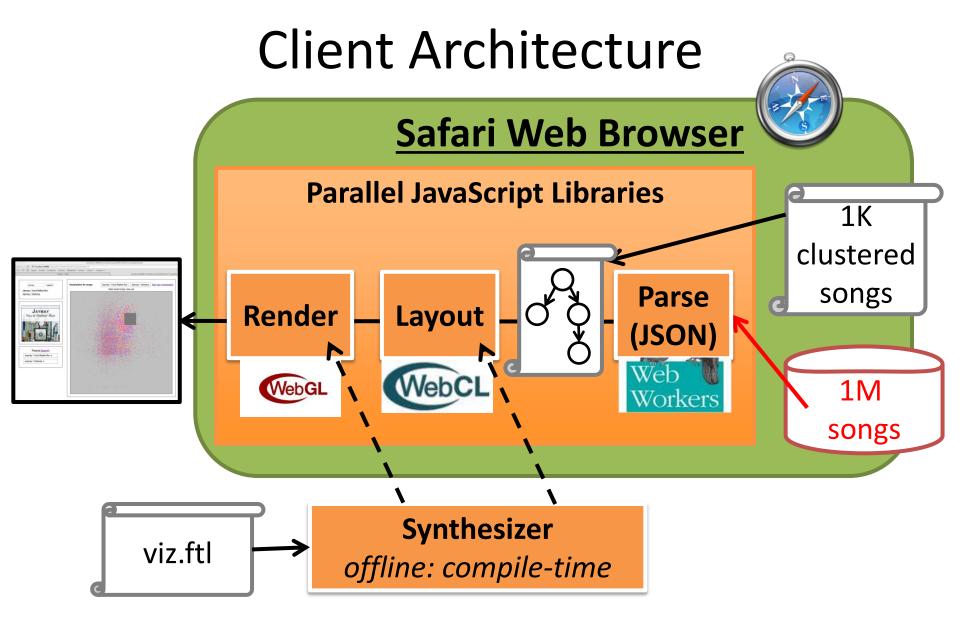
End of ParLab Celebration May 30, 2013



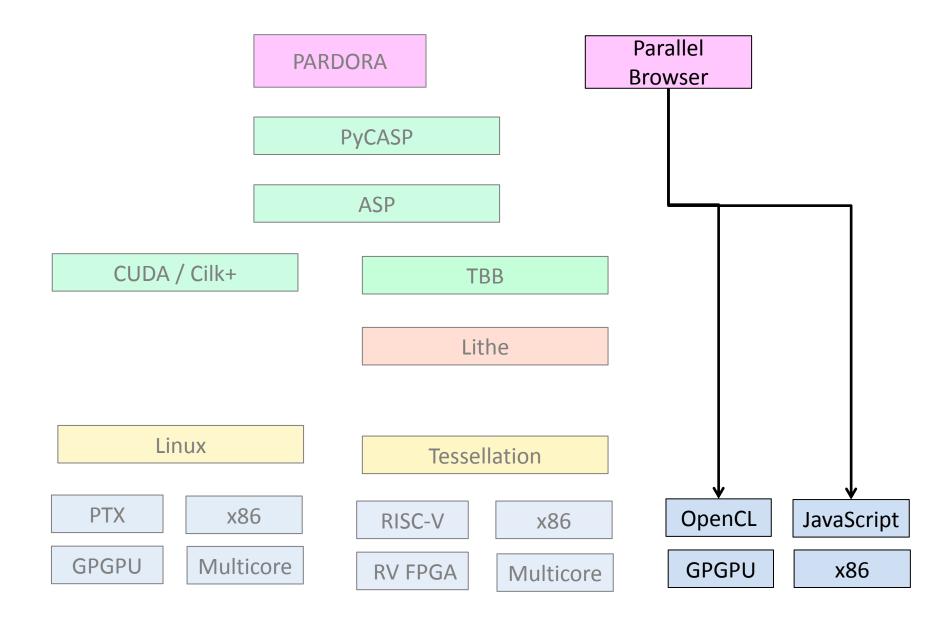


## Demo

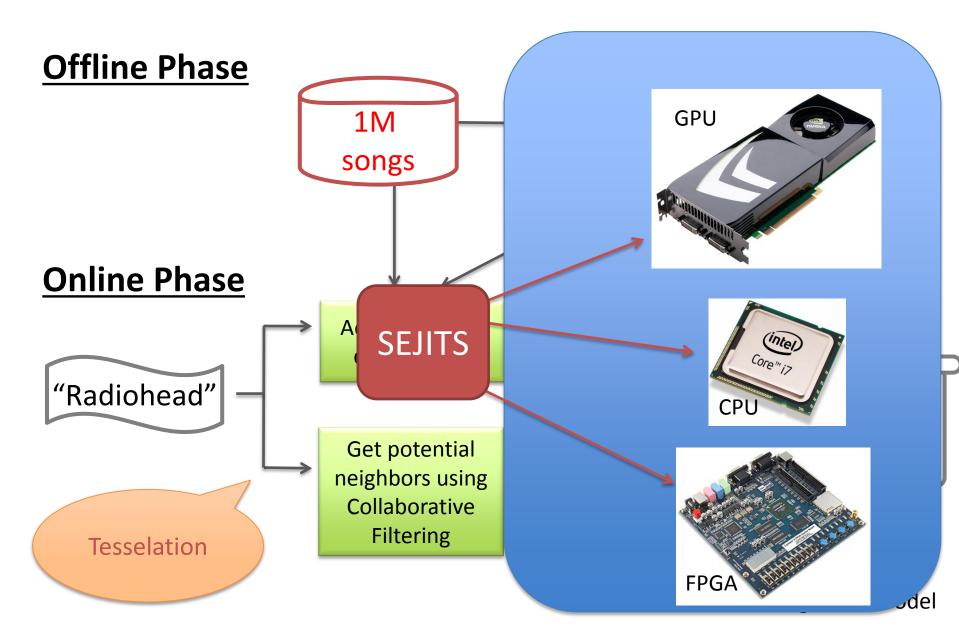




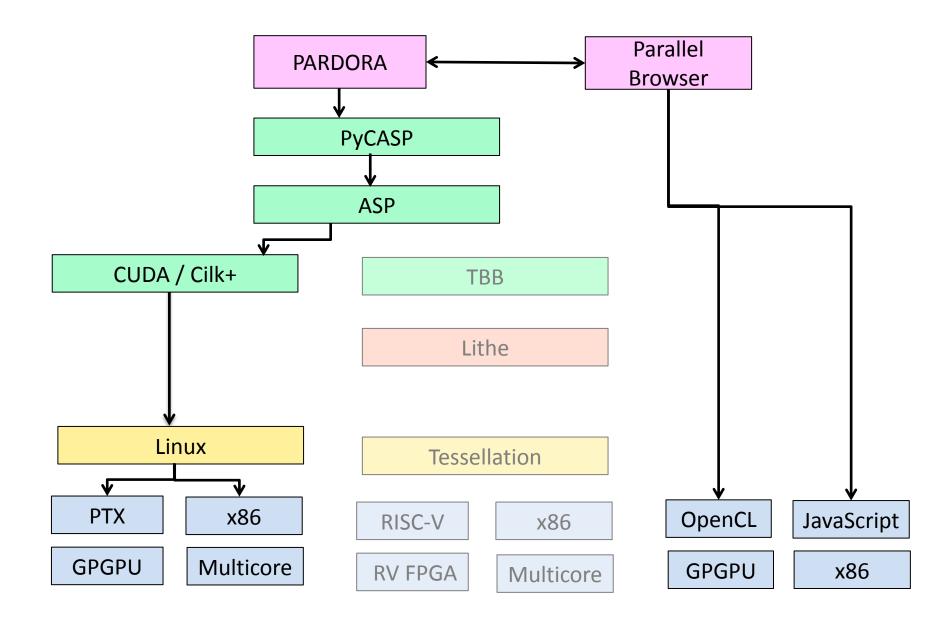
#### Music Recommendation Stack



#### Server Architecture



#### Music Recommendation Stack



#### Music Recommendation Stack

